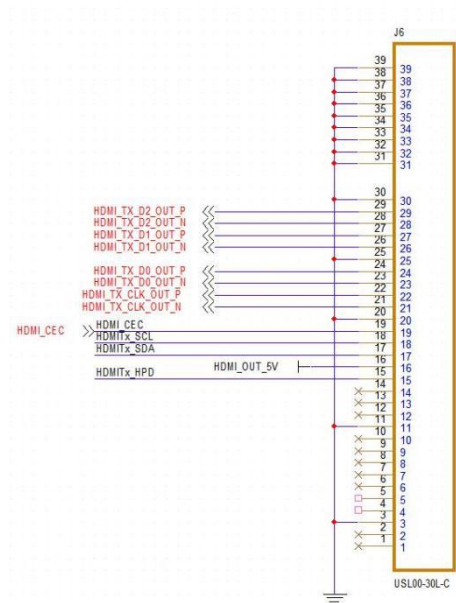


J1 Interface (HDMI Output): (Model: USL00-30L-C 30Pin 0.4mm pitch horizontal pin header)
Marked with "HDMI_OUT"

Circuit Design:

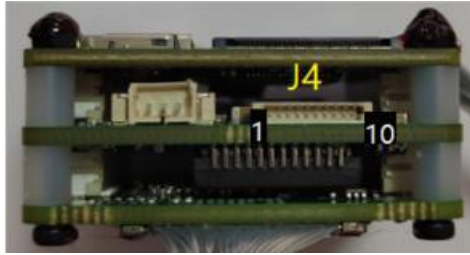


J2 Interface (Sony Input): (Model: USL00-30L-C 30Pin 0.4mm pitch horizontal pin header)
Connects to Sony LVDS output series modules, marked with "SONY_IN"

J3 Interface (Power Communication): (Model: SH1.0_6P 1mm pitch horizontal pin header)

Pin Definitions:

Pin	Signal	Definition	Direction
1	Power Supply +	+12V	
2		+12V	
3	Ground	GND	
4		GND	
5	Communication Port	RXD	Tracker receiving signal
6		TXD	Tracker sending signal



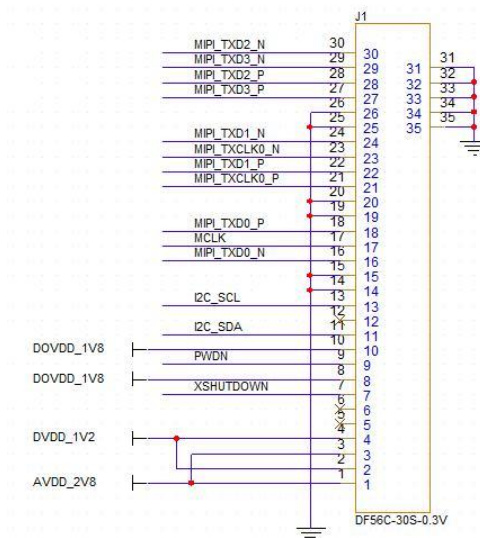
J4 Interface (Gigabit Ethernet): (Model: BM10B-SURS-TF 0.8mm pitch horizontal pin header)

Pin Definitions:

Pin	Signal	Definition
1	Network	MDI2-
2		MDI2+
3		MDI1-
4		MDI1+
5		MDI3-
6		MDI3+
8		MDI4-
9		MDI4+
7, 10		Ground

J5 Interface (MIPI Input): (Model: DF56C-30S-0.3V)

Circuit Design

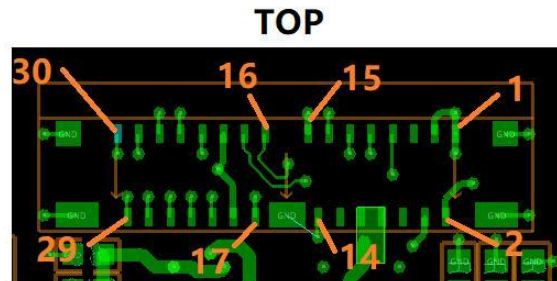


Explanation:

1. Camera output MIPI format required is CSI, with a standard interface of one set of clock and four sets of data.
2. I2C, MCLK, PWDN, XSHUTDOWN signals are reserved.
3. 1.8V, 1.2V, 2.8V power supplies are reserved.
4. Provide cooperation for customized visible light fixed focus modules.

PCB Design

Top View

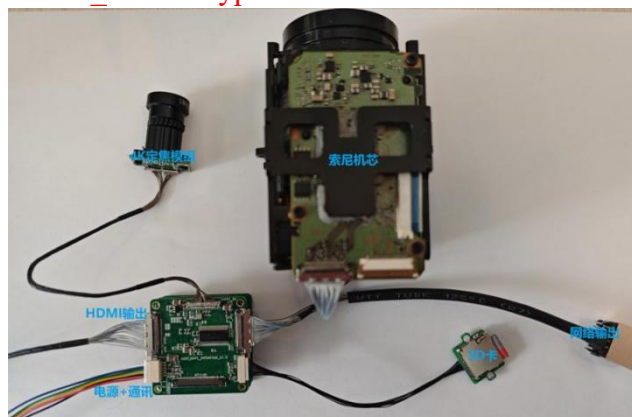


J6 Interface (SD Card Slot): (Model: BM10B-SURS-TF 0.8mm pitch horizontal pin header) (SD card extension board can be provided)

Pin Definitions:

Pin	Signal	Definition
1	SDIO Signal	SDIO0_CARD_DETECT
2	SDIO Signal	SDIO0_CDATA2
3	SDIO Signal	SDIO0_CDATA3
4	SDIO Signal	SDIO0_CCMD
5	Power Supply	3.3V
6	SDIO Signal	SDIO0_CCLK_OUT
7	Ground	GND
8	SDIO Signal	SDIO0_CDATA0
9	SDIO Signal	SDIO0_CDATA1
10	Ground	GND

USB Interface: MICRO_USB B-type female connector



Wiring Diagram